Application No.: 10/650,105 Docket No.: 10970696-3

## **AMENDMENTS TO THE SPECIFICATION**

Please add the following title and paragraph beginning at line 1, page 1 of the specification:

## CROSS REFERENCE TO RELATED APPLICATIONS

This application is a continuation of U.S. Patent Application Serial Number 08/881,196, filed on June 24, 1997, entitled "VIRTUAL MEMORY TRANSLATION CONTROL BY TLB PURGE MONITORING," issued as U. S. Patent No. 6,668,314 on December 23, 2003.

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